

JUN 01 2004

U.S. PATENT & TRADEMARK OFFICE

Certificate of Mailing under 37 CFR 1.8

hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: May 24, 2004

Michael Navarro
Michael Navarro

Docket No. PWV1.PAU.49

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gurvich et al.

Examiner: Henry Choe

Serial No.: 10/633,984

Art Unit: 2817

Filed: August 4, 2003

Title: Enhanced Efficiency LDMOS
Based Feed Forward Amplifier

RESPONSE TO OFFICE ACTION

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This amendment is being filed in response to the Office Action dated March 17, 2004. Although no extension of time is necessary, if any fee or extension may be required for consideration of this amendment, such is hereby requested. Please charge any deficit or credit any surplus to Deposit Account No. 01-1960.

05/24/2004 JMC/HLLA 00000001 011960 10633984

01 FC:1806 180.00 CR

Adjustment date: 10/29/2004 SDIRETA1
06/14/2004 JMC/HLLA 00000001 011960 10633984
01 FC:1806 180.00 CR

MYERS DAWES ANDERSON & SHERMAN LLP

An International Patent Law Firm

August 25, 2004

Deposit Account 011960

06/14/04 Ref. 10633984 Docket No. PWV1.PAU.49

The Information Disclosure Statement filed on May 26, 2004 was reiterating the Information Disclosure Statement filed with a counterpart PCT application (see attached). Therefore, no fee was required. Please issue a credit for \$180.00.

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with postage paid and that the enclosed class mail in an envelope addressed to the Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 26, 2004.


Michael Novano

PATENT
PWV1:PAU.49

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gurvich et al.

Serial No.: 10/633,984

Art Unit: 2817

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Examiner: Henry Choe

Title: Enhanced Efficiency LDMOS
Based Feed Forward
Amplifier

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

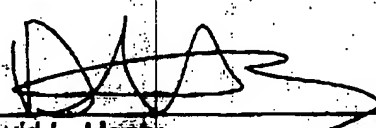
Sir:

In accordance with 37 C.F.R. § 1.56, Applicant(s) submit herewith the attached Information Disclosure Statement Form PTO-1449 in connection with the above-identified patent application. The statement is not a representation that all of the information cited is necessarily effective as prior art against the application.

Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart PCT application not more than three months prior to the filing of the Information Disclosure Statement. Therefore, it is respectfully submitted no fee is required. If any fee is required for consideration of this Information Disclosure Statement please charge Deposit Account No. 01-1960.

Respectfully submitted,

Date: May 26, 2004


David L. Henry
Attorney for Applicant(s)
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